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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO. CONFIRMATION NO		
10/727,138	12/03/2003	Kaushik Saha	852463.406	5322	
	7590 04/01/200 ECTUAL PROPERTY	EXAMINER			
701 FIFTH AVENUE, SUITE 5400 SEATTLE, WA 98104-7092			DO, CHAT C		
SEATTLE, WA	1 9010 4- /092		ART UNIT	PAPER NUMBER	
			2193		
			MAIL DATE	DELIVERY MODE	
			04/01/2008	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
10/727,138	SAHA ET AL.		
Examiner	Art Unit		
CHAT C. DO	2193		

	CHAT C. DO		2193	
The MAILING DATE of this communication appe	ars on the cover	sheet with the d	correspondence add	ress
THE REPLY FILED <u>11 March 2008</u> FAILS TO PLACE THIS AP	PLICATION IN CO	NDITION FOR	ALLOWANCE.	
1. The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following rapplication in condition for allowance; (2) a Notice of Appe for Continued Examination (RCE) in compliance with 37 C periods:	replies: (1) an ame eal (with appeal fee	ndment, affidavi e) in compliance	t, or other evidence, w with 37 CFR 41.31; or	hich places the (3) a Request
a) The period for reply expiresmonths from the mailing b) The period for reply expires on: (1) the mailing date of this Adno event, however, will the statutory period for reply expire la Examiner Note: If box 1 is checked, check either box (a) or (I MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f	dvisory Action, or (2) ater than SIX MONTI b). ONLY CHECK B	the date set forth IS from the mailing	g date of the final rejectio	n.
Extensions of time may be obtained under 37 CFR 1.136(a). The date of have been filed is the date for purposes of determining the period of extrumer 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	ension and the corre hortened statutory po than three months a	sponding amount origi	of the fee. The appropria nally set in the final Offic	te extension fee e action; or (2) as
2. The Notice of Appeal was filed on A brief in compl filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed wi AMENDMENTS	nsion thereof (37 C	FR 41.37(e)), to	avoid dismissal of the	
3. The proposed amendment(s) filed after a final rejection, be (a) They raise new issues that would require further core (b) They raise the issue of new matter (see NOTE below (c) They are not deemed to place the application in bett appeal; and/or (d) They present additional claims without canceling a content of the property of the	nsideration and/or w); ter form for appeal	search (see NOT	ΓE below); ducing or simplifying th	
NOTE: (See 37 CFR 1.116 and 41.33(a)). 4. The amendments are not in compliance with 37 CFR 1.12 5. Applicant's reply has overcome the following rejection(s): 6. Newly proposed or amended claim(s) would be allowed.	21. See attached N	lotice of Non-Co	mpliant Amendment (F	
non-allowable claim(s). 7. For purposes of appeal, the proposed amendment(s): a) [how the new or amended claims would be rejected is prov The status of the claim(s) is (or will be) as follows: Claim(s) allowed: Claim(s) objected to: Claim(s) rejected: 1-7 and 10-20. Claim(s) withdrawn from consideration:			l be entered and an ex	xplanation of
AFFIDAVIT OR OTHER EVIDENCE				
 The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e). 	d sufficient reasons	why the affidavi	t or other evidence is	necessary and
9. The affidavit or other evidence filed after the date of filing a entered because the affidavit or other evidence failed to or showing a good and sufficient reasons why it is necessary	vercome <u>all</u> rejecti	ons under appea	al and/or appellant fails	to provide a
10. ☐ The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER			•	
 11. The request for reconsideration has been considered but See Continuation Sheet. 12. Note the attached Information Disclosure Statement(s). (·		condition for allowand	ce because:
13. Other:	. 10/05/00/1 аре	. 140(3).		
	/Chat C. Primary E	Do/ xaminer, Art U	nit 2193	

Continuation of 11. does NOT place the application in condition for allowance because: The applicant argued in page 7 fourth paragraph for claims rejected under 35 U.S.C. 101 that the current amended version would overcome the rejection by processing a digital signal. In addition claim 3 is not a software claim since recites means for storing the inputs and outputs of the system to process a digital signal. The examiner respectfully subsmit that the current amendment does not overcome the 101 rejection as cited in the previous Office action since the claims just merely disclose series steps for performing FFT/IFFFT without clearly disclosing a practical application. Further the claims appear to preemtp every substantial practical application of the idea emobided by the claims. Claim 3 is considered as a software per se since every means is software module for performing the intended function. Nothing in the claim would indicate or utilize hardware to perform the intended function rather software modules.

The applicant argued in page 7 for claims 5-6 that there is no explaination of why claims 5 and 16 are directed to non-statutory subject matter.

The examiner respectfully submits that the previous Office action clearly address how and why these claims are directed to non-statutory subject matter. IN ADDITION to the above rejection under 35 U.S.C. 101, claims 5 and 16 merely disclose a computer readable medium without clearly address that the medium is tangible and executed by a computer. Without executing by a computer, the computer-readable medium is just a storage which cannot perform the intended functions.

The applicant argued in page 8 for claims rejected under 35 U.S.C. 103(a) that the cited references by Abel and Jaber fail to disclose the "linear scalable" method wherein the specification defines the "linear scalability" as the computation time reducing in inverse proportion to the number of processors in the multiprocessor solution and the step of "distributing...in the stage" would support the linear scalable method as claimed.

The examiner respectfully submits that the definition of linear scalability as "the computation time reducing in inverse proportion to the number of processors in the multiprocessor solution" is addressed in the original specification, but not in the claim. In addition, the step "distributing...in the stage" has no direct correlated to the definition of the "linear scalability" as "the computation time reducing in inverse proportion to the number of processors in the multiprocessor solution". Thus, the "linear scalable" is not given any patentable weight because it is recited in the preamble of the claim. In general, the combination of references by Abel and Jaber clearly discloses reasonably every single limitations cited in the claims either individually or in combination.

The applicant argued in pages 9-10 for claims 1, 3, and 5 that neither Abel nor Jaber teach, suggest or motivate a linear scalable method comprising a first plurality of stages employing a plurality of butterfly operations having a first radix, wherein each of the butterfly operations in each stage in the first plurality of stages has a single, un-nested computation loop of the first radix as cited in the claimed invention. The examiner respectfully submits that this particular features are very common/standard in FFT as clearly seen in primary reference by Abel et al.'s Figures 1-14. Abel et al. disclose in Figures 1-14 a linear scalable method for computing a Fast Fourier Transform (FFT) or Inverse Fast Fourier transform (IFFT) in a system (e.g. abstract, Figures 7 and 11 wherein Figure 7 discloses an IFFT and Figure 11 discioses a FFT) using a decimation in time approach (e.g. last line of abstract and col. 13 line 65 to col. 14 line 12), comprising the steps of: computing an N-point FFT/IFFT of a signal (e.g. either seen in Figures 7-8 or Figure 11 for IFFT/FFT respectively) using a first plurality of butterfly computational stages (e.g. Figure 4 and Figure 8 wherein the first plurality of butterfly is performed in components 800 and 805), each stage in the first plurality of stages employing a plurality of butterfly operations having a first radix (e.g. Figure 8 wherein components 800 and 805 each utilizes radix-2 as the first radix size) wherein each of the butterfly operations in each stage (e.g. components 800, 805, and 810 in Figure 8) in the first plurality of stages has a single, un-nested computation loop of the first radix (e.g. Figure 4 and Figure 8 wherein there is no loopback/feedback for computing the IFFT/FFT).